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03/29/02

PATENT NUMBER and
ISSUE DATE

U.S. UTILITY Patent Application

APPL NUM	FILING DATE	CLASS	SUBCLASS	GAU	EXAMINER
10063212	03/29/2002	365		2818	/
**APPLICANTS: Barry Robert, Croce Peter, Eushis Steven, Wang Yabin					
**CONTINUING DATA VERIFIED: no LP					
** FOREIGN APPLICATIONS VERIFIED: no LP					
PG PUB		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no		ATTORNEY DOCKET NO	
35 USC 119 conditions met		<input type="checkbox"/> yes <input checked="" type="checkbox"/> no		BUR920010182	
Verified and Acknowledged Examiner's initials		LP			
TITLE : Complementary two transistor ROM cell					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Total Claims	Print Claim for O.G
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Sheets Drwg.	Figs. Drwg. Print Fig.
		Application Examiner	
<input type="checkbox"/> TERMINAL DISCLAIMER		PREPARED FOR ISSUE	
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FILED WITH:

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